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09/328910

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PTO/SB/50 (4/98)

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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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REISSUE PATENT APPLICATION TRANSMITTAL

Address to:

Assistant Commissioner for Patents
Box Reissue
Washington, DC 20231

Attorney Docket No.	500.32049R00
First Named Inventor	KAJIHARA et al.
Original Patent Number	5,637,913
Original Patent Issue Date (Month/Day/Year)	June 10, 1997
Express Mail Label No.	

APPLICATION FOR REISSUE OF:
(check applicable box)

Utility Patent



Design Patent



Plant Patent

APPLICATION ELEMENTS

1. ☒ *Fee Transmittal Form (PTO/SB/56)
(Submit an original, and a duplicate for fee processing)
2. ☒ Specification and Claims (amended, if appropriate)
24 PAGES
3. ☒ Drawing(s) (proposed amendments, if appropriate)
21 SHEETS
4. ☐ Reissue Oath / Declaration (original or copy)
(37 C.F.R. § 1.175)(PTO/SB/51 or 52)
5. Original U.S. Patent
☐ Offer to Surrender Original Patent (37 C.F.R. § 1.178)
(PTO/SB/53 or PTO/SB/54)
or
☐ Ribbonded Original Patent Grant
☐ Affidavit / Declaration of Loss (PTO/SB/55)
6. Original U.S. Patent currently assigned?
☒ Yes ☐ No
(If Yes, check applicable box(es))
☐ Written Consent of all Assignees (PTO/SB/53 or 54)
☐ 37 C.F.R. § 3.73(b) Statement ☐ Power of Attorney

ACCOMPANYING APPLICATION PARTS

7. ☒ Foreign Priority Claim (35 U.S.C. 119)
(if applicable)
8. ☐ Information Disclosure Statement (IDS)/PTO-1449 ☐ Copies of IDS Citations
9. ☐ English Translation of Reissue Oath/Declaration
(if applicable)
* Small Entity ☐ Statement filed in prior application,
Status still proper and desired (PTO/SB/09-12)
10. ☐ Statement(s)
11. ☐ Preliminary Amendment
12. ☒ Return Receipt Postcard (MPEP 503)
(Should be specifically itemized)
13. ☐ Other:

*NOTE FOR ITEMS 1 & 10: IN ORDER TO BE ENTITLED TO PAY SMALL ENTITY FEES, A SMALL ENTITY STATEMENT IS REQUIRED (37 C.F.R. § 1.27), EXCEPT IF ONE FILED IN A PRIOR APPLICATION IS RELIED UPON (37 C.F.R. § 1.28).

14. CORRESPONDENCE ADDRESS

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT REQUIRED

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: KAJIHARA et al.

Reissue Application of: U.S. Patent 5,637,913; issued June 10, 1997

Filed: Concurrently Herewith

For: LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
USING THE SAME, AND METHOD OF AND PROCESS FOR
FABRICATING THE TWO

Art Unit: Unassigned

Examiner: Unassigned

CLAIM FOR PRIORITY

Assistant Commissioner
for Patents
Washington, D.C. 20231

June 9, 1999

Sir:

Pursuant to 35 USC 119 and 37 CFR §1.55, the Applicant(s) hereby claim the right
of priority based on:

Japanese priority document No. 4-320098, filed November 30, 1992, in Japan

Japanese priority document No. 4-071116, filed March 27, 1992, in Japan.

Certified copies of these foreign applications were filed on March 29, 1993, in
application Serial No. 08/038,684 which issued as U.S. Patent No. 5,637,913, the patent
of which the present application is a reissue.

Respectfully submitted,



Paul J. Skwierawski

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Attachments

09328910-060999